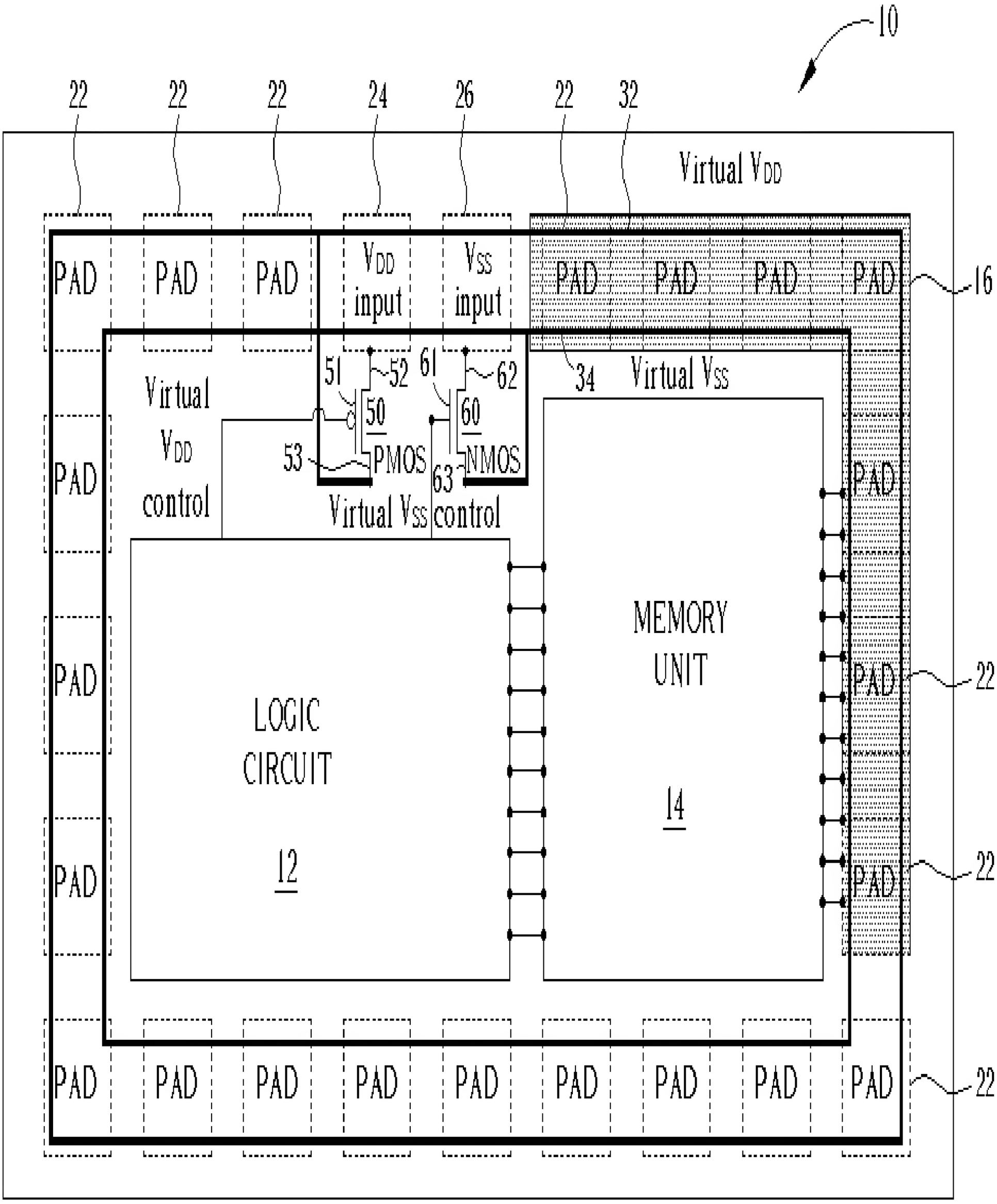
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22
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22
32
Virtual V_{DD}
PAD
PAD
PAD
V_{DD} input
V_{SS} input
PAD
PAD
PAD
PAD
16
51
52
61
62
34
Virtual Vss
Virtual V_{DD} control
50
PMOS
60
NMOS
53
63
Virtual Vss control
PAD
PAD
PAD
LOGIC CIRCUIT
12
MEMORY UNIT
14
PAD
22
PAD
22
PAD
PAD
PAD
PAD
PAD
PAD
PAD
PAD
PAD
22

Fig. 1